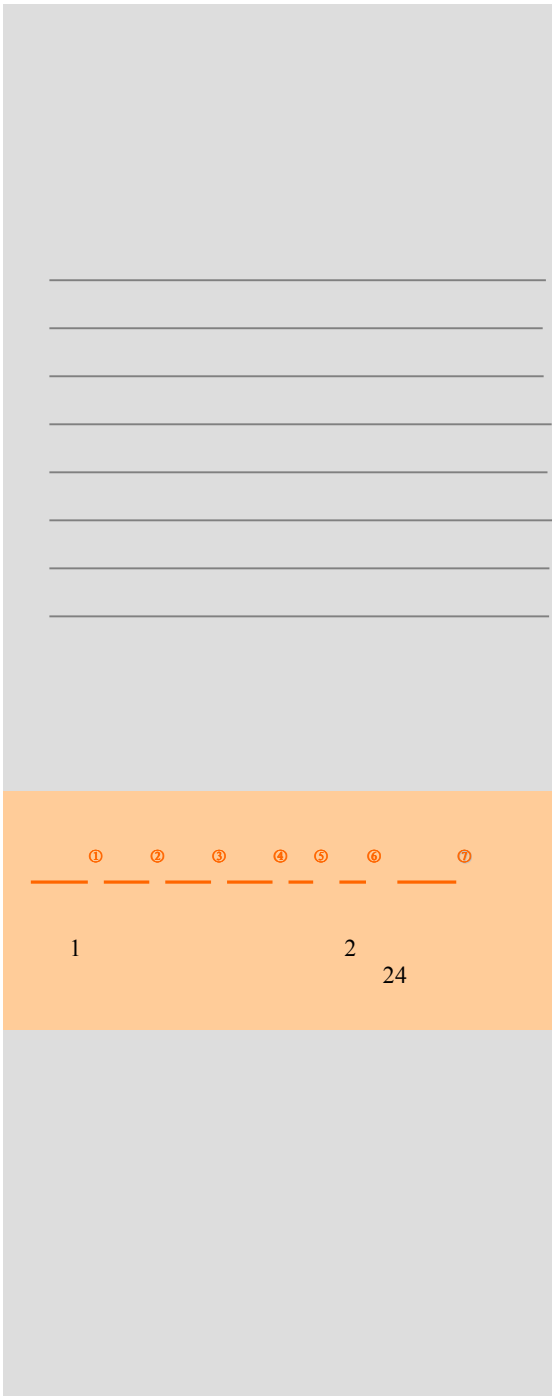


**6W, FIXED INPUT ISOLATED & REGULATED TWIN OUTPUT DIP24 DC-DC CONVERTER**



Pin Configuration							
Pin No.	Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7
1							
2							
3							
4							
5							
6							
7							
8							
9							
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11							
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24							

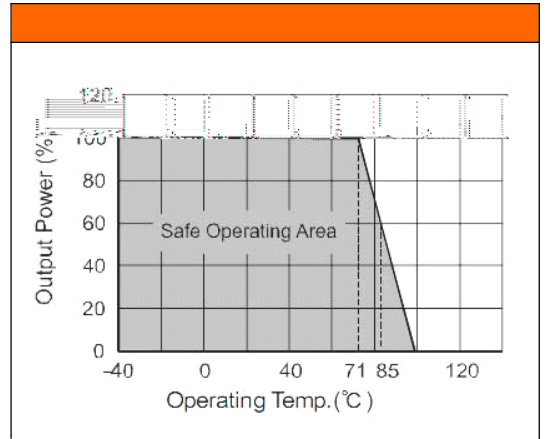


**CE REACH**

Item	Test Conditions	Min.	Typ.	Max.	Units

Item	Test Conditions	Min.	Typ.	Max.	Units

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“ ”



**(Figure 1)** Circuit diagram of a DC-DC converter. It shows an input stage with Vin, Cin, and GND. The converter consists of two DC blocks. The output stage has two outputs: +Vo1 and +Vo2, each with a Cout capacitor and a 0V reference point.

**(Figure 2)** Graph of Input Current (A) vs Input Voltage (V). The curve shows a peak current Ip. A shaded region indicates the 'Input Voltage Range' where the current is relatively constant.

**1) Requirement On Output Load**

**2) Recommended Circuit**

**3) Input Current**

**4) No parallel connection or plug and play**

Vout(VDC)	Cout(uF)

&

